

## Claim Listing

1. (Previously Presented) A method for decoding video data, said method comprising:

writing one or more start codes to a start code table; and

writing presentation time information to the start code table; and

wherein the start code table comprises a plurality of data words and wherein writing one or more start codes further comprises writing a plurality of start codes to a particular one of the plurality of data words in the start code table.

2. (Original) The method of claim 1, wherein the presentation time information comprises a presentation time stamp.

3. (Original) The method of claim 1, further comprising writing decoding time information to the start code table.

4. (Original) The method of claim 3, wherein the decoding time information comprises a decoding time stamp.

5. (Cancelled).

6. (Previously Presented) The method of claim 1, wherein the plurality of start codes comprises a slice start code and a non-slice start code.

7. (Previously Presented) The method of claim 1, further comprising:

writing a command to the start code table.

8. (Original) The method of claim 7, further comprising:

writing a reference clock offset to the start code table.

9. (Original) The method of claim 8, wherein the command and the reference clock offset are written to another particular one of the plurality of data words.

10. (Original) A circuit for decoding video data, said circuit comprising:

a start code table for storing start codes, the start code table comprising a plurality of data words; and

a video transport processor for writing a plurality of start codes to a particular data word in the start code table.

11. (Original) The circuit of claim 10, wherein the plurality of start codes comprises a slice start code and a non-slice start code.

12. (Original) The circuit of claim 10, wherein the video transport processor writes presentation time information to the start code table.

13. (Original) The circuit of claim 12, wherein the presentation time information comprises a presentation time

stamp.

14. (Original) The circuit of claim 10, wherein the video transport processor writes decoding time information to the start code table.

15. (Original) The circuit of claim 14, wherein the decoding time information comprises a decoding time stamp.

16. (Original) The circuit of claim 10, wherein the video transport processor writes a reference clock offset to the start code table.

17. (Original) The circuit of claim 16, wherein the video transport processor writes a command to the start code table.

18. (Original) The circuit of claim 17, wherein the command and the reference clock offset are written to another particular one of the plurality of data words.

19. (Previously Presented) An article of manufacture comprising a computer readable medium, said machine readable medium storing a plurality of executable instructions, said plurality of executable instructions for:

writing one or more start codes to a start code table; and

writing presentation time information to the start code table;

wherein the start code table comprises a

plurality of data words and wherein writing one or more start codes further comprises writing a plurality of start codes to a particular one of the plurality of data words in the start code table.

20. (Original) The article of manufacture of claim 19, wherein the presentation time information comprises a presentation time stamp.

21. (Original) The article of manufacture of claim 19, wherein the plurality of instructions further comprises instructions for writing decoding time information to the start code table.

22. (Original) The article of manufacture of claim 21, wherein the decoding time information comprises a decoding time stamp.

23. (Cancelled)

24. (Original) The article of manufacture of claim 23, wherein the plurality of start codes comprises a slice start code and a non-slice start code.

25. (Original) The article of manufacture of claim 24, wherein the plurality of instructions further comprises instructions for:

writing a command to the start code table.

26. (Original) The article of manufacture of claim 25, wherein the plurality of instructions further comprises

instructions for:

writing a reference clock offset to the start code table.

27. (Original) The article of manufacturer of claim 26, wherein the command and the reference clock offset are written to another particular one of the plurality of data words.

28. (New) The circuit of claim 10, wherein the plurality of start codes written to the particular data word comprise a start code for a slice group and a start code for a picture.